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- 10. (currently canceled)
- 11. (currently canceled)
- 12. (currently canceled)

13. (currently amended) The method of Claim 11,

further comprising: A method of operating a power amplifier

comprising:

receiving a radio frequency (RF) input signal with a first amplifier output stage and a second amplifier output stage;

enabling the first amplifier output stage and disabling the second amplifier output stage during a low power operating mode of the power amplifier;

enabling the first amplifier output stage and the second amplifier output stage during a high power operating mode of the power amplifier;

biasing a first set of transistors in the first amplifier output stage with a common bias reference circuit and a plurality of distributed bias circuits; and

biasing a second set of transistors in the second amplifier output stage with a common bias reference circuit and a plurality of distributed bias circuits.

- 14. (original) The method of Claim 17, wherein there are more transistors in the second set of transistors than in the first set of transistors.
- 15. (currently amended) The method of Claim 11, further comprising: A method of operating a power amplifier comprising:

receiving a radio frequency (RF) input signal with

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